

Application Serial No. 10/625,287

PATENT
Docket: CU-3301
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REMARKS/ARGUMENTS

AUG 09 2007

Reconsideration is respectfully requested.

Pending in this patent application are claims 1-10 before this amendment. By the present amendment, claim 2 is amended. No new matter has been added.

As to the FIG. 3 and the amendments to the specification, the applicants respectfully direct the examiner's attention to specification page 9, lines 4-18, according to which the first switches "SW1-SW29" and first shift registers "SR1-SR30" are included in the first shift register section. The corrected FIG. 3 reflects this disclosure of the specification.

The specification paragraphs [0034] and [0035] (i.e., page 10, line 18 to page 11 line 23), the second switches has been renumbered from "SW30" to "SW58" in the second shift register section in which there are "SR31-SR60" second shift registers. Although the switch numbering has been corrected in the second shift register section, the "SR31-SR60" second shift register designation has not been changed. Thus, as shown in the corrected FIG. 3 and consistent with paragraphs [0034]-[0035], SR31 is a part of the second shift register section, and also as shown in FIG. 3 where the renumbered switch "SW30" is connected to SR32. The applicants respectfully submit that these renumbering of switches in the second shift register section to begin from SW30 to follow from the last numbered SW29 in the first shift register section is not addition of any new matter.

Further, the examiner is respectfully directed to specification page 12, lines 1-14, which is one among many paragraphs that supports the amendment to FIG. 3.

[0036] In order to sequentially scan a predetermined number of gate bus lines during the active address interval in response to the vertical clock signal CPV, the second shift register section 240 having such construction receives a shifted signal by the shift register SR30 of the first shift register section 220 and then outputs it through the shift registers SR31~SR60 after sequentially shifting it. Further, in order to scan the predetermined

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number of gate bus lines at the same time during the vertical blanking interval, **the second shift register section 240 receives the shifted signal by the shift register SR30 of the first shift register section 220 and then outputs a predetermined number of output signals at the same time through the shift registers SR31 SR60.**

As disclosed, the applicants respectfully submit that the **second shift register section 240 receives the shifted signal by the shift register SR30 of the first shift register section 220 and then outputs a predetermined number of output signals at the same time through the shift registers SR31-SR60.** As such, the switch connected to the SR31 in the original FIG. 3 was in an incorrect representation of the disclosure, which has been removed by the corrected FIG. 3, and as such the switches in the second shift register section connected to SR32-SR60 has been renumbered to SW30 and SW58.

Further, claim 2 has been amended to remove all issues relating to lack of enablement.

As described in the specification page 4, lines 8-12 [¶0012], the blurring problems are present in the conventional art. The specification page 4, lines 13-23 [¶¶0012-0013] describes that the conventional device cannot be driven at 85 Hz due to various conventional problems including blurring. The presently claimed invention provides solutions to this and other conventional problems, and claim 2 recites this one aspect of the present invention where the liquid crystal device can cause fast moving images without blurring at the refresh rate of 60 Hz. Claim 2 is supported in the specification page 13, lines 12-16, [¶0040] and page 15, line 18, to page 16, line 11 [¶¶0046-0047], according the which the blurring phenomenon is removed since black data can be sufficiently inserted within the vertical blanking interval.

To better clarify, claim 2 has been amended to recite: —wherein the active address interval is substantially equivalent to being driven at 85 Hz **when although**

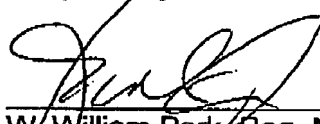
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a refresh rate is 60 Hz--.

In the office action, claims 1 and 3-10 stand allowed.

For the reasons set forth above, issuance of a Notice of Allowance is respectfully requested in the next action. Should the examiner have any remaining questions or concerns, the examiner is encouraged to contact the undersigned attorney by telephone to expeditiously resolve such concerns.

Respectfully submitted,



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